

PINOUT Information of Vector Quantizer Chip

Pin Number	Signal Name	Pin Number	Signal Name
1	GND _{DIGITAL}	43	N/C
2	uP_write	44	OUT<1>
3	uP_read	45	OUT<0>
4	uP_cs	46	I _{TAIL}
5	uP_adress<0>	47	V _{IB}
6	uP_adress<1>	48	V _{REGC}
7	uP_adress<2>	49	V _{IN-<17>}
8	uP_adress<3>	50	IN<15>
9	VDD _{DIGITAL}	51	V _{IN+<17>}
10	GND _{DIGITAL}	52	IN<11>
11	uP_adress<4>	53	IN<12>
12	uP_adress<5>	54	IN<13>
13	uP_adress<6>	55	IN<14>
14	uP_adress<7>	56	GND _{DCELL}
15	VDD _{DIGITAL}	57	GND _{DCELL}
16	GND _{DIGITAL}	58	VDD _{DCELL}
17	uP_adress<8>	59	VDD _{DCELL}
18	uP_adress<9>	60	IN<10>
19	uP_data<0>	61	IN<9>
20	uP_data<1>	62	IN<8>
21	N/C	63	V _{REF_INAMP}
22	N/C	64	V _{ISF}
23	VDD _{DIGITAL}	65	IN<7>
24	GND _{DIGITAL}	66	IN<6>
25	uP_data<2>	67	IN<5>
26	uP_data<3>	68	GND _{INAMP}
27	uP_data<4>	69	GND _{INAMP}
28	uP_data<5>	70	VDD _{INAMP}
29	VDD _{DIGITAL}	71	VDD _{INAMP}
30	GND _{DIGITAL}	72	IN<4>
31	uP_data<6>	73	IN<3>
32	uP_data<7>	74	IN<2>
33	clk	75	IN<1>
34	reset	76	IN<0>
35	VDD _{DIGITAL}	77	V _{IN-<0>}
36	GND _{DIGITAL}	78	V _{IN+<0>}
37	OUT<5>	79	V _{BIAS}
38	OUT<4>	80	RN
39	OUT<3>	81	RP
40	OUT<2>	82	VDD _{DIGITAL}
41	VDD _{DIGITAL}	83	N/C
42	GND _{DIGITAL}	84	N/C