

**Design of a fully differential current mode
opamp with improved input-output
impedances and its filter applications**

İlker YAĞLIDERE

504081212

09.11.2009

Current Mode Analog Circuit Design



CONTENT

1. OPERATIONAL AMPLIFIERS
 - 1.1. Voltage Opamp (VOA)
 - 1.2. Current Opamp (COA)
 - 1.3. Current Feedback Opamp (CFOA)
 - 1.4. Transconductance Opamp (OTA)
2. THE ADJOINT NETWORK THEOREM
3. A NOVEL COA WITH IMPROVED INPUT/OUTPUT IMPEDANCES
4. COA BASED FILTERS

WHY CURRENT MODE?

In recent years, current-mode approach has been extensively investigated. It has some advantages [1,2]:

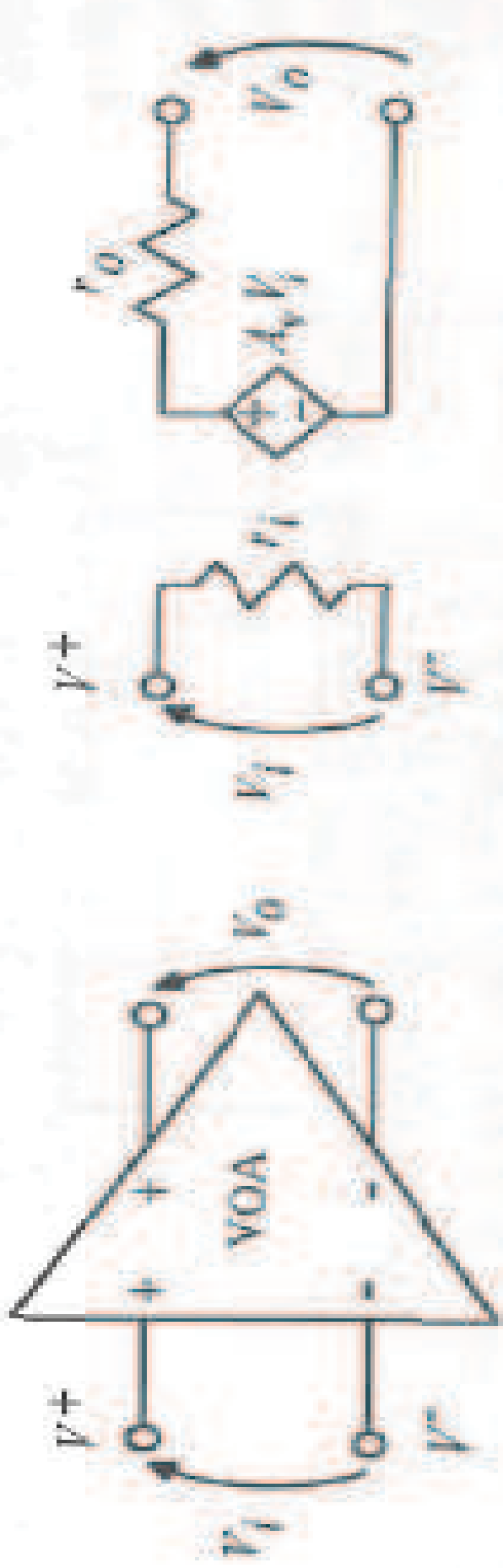
1. Wide bandwidth
2. Wide dynamic range
3. Simple circuitry
4. Low voltage design

OPERATIONAL AMPLIFIERS

1. Voltage opamp (VOA)
2. Current opamp (COA)
3. Current feedback opamp (CFOA)
4. Transconductance opamp (OTA)

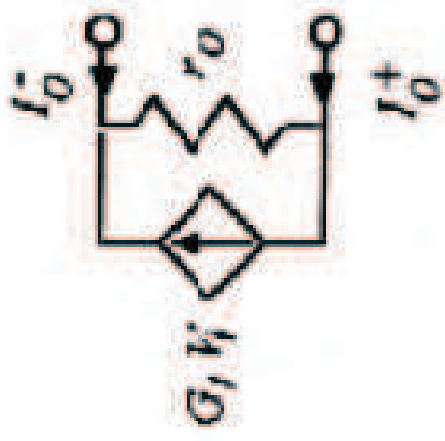
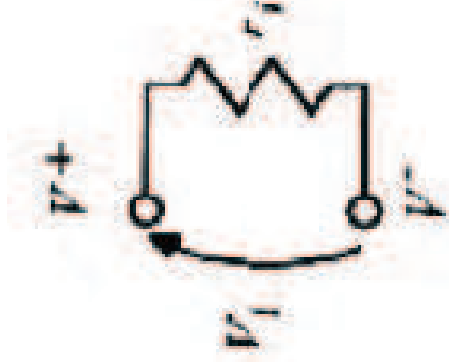
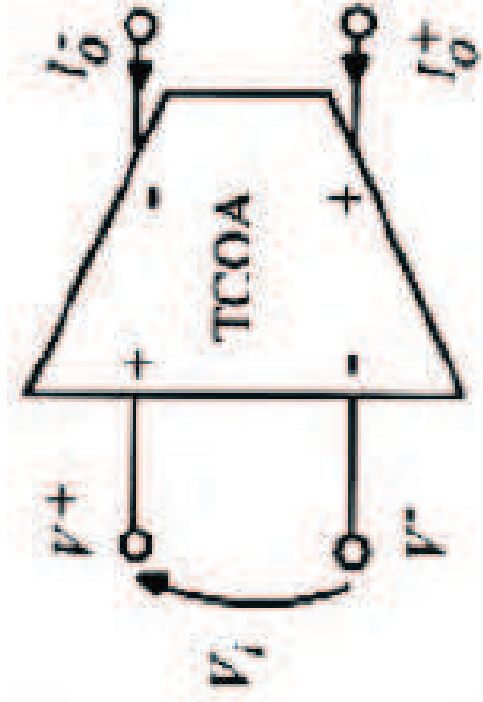
VOLTAGE OPAMP (VOA)

Voltage opamp(VOA): Voltage controlled voltage source with infinite voltage gain and input resistance, zero output resistance[3].



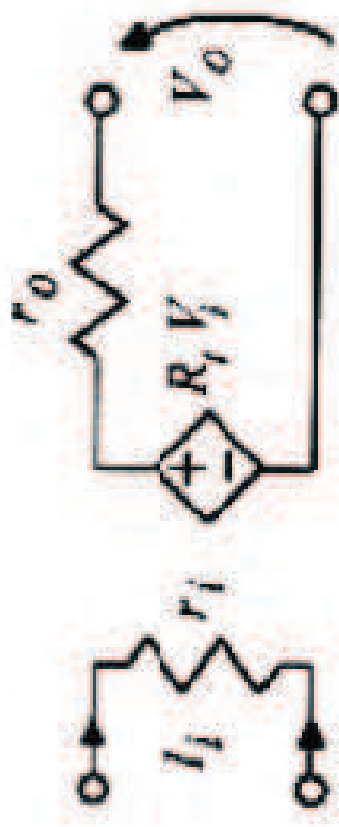
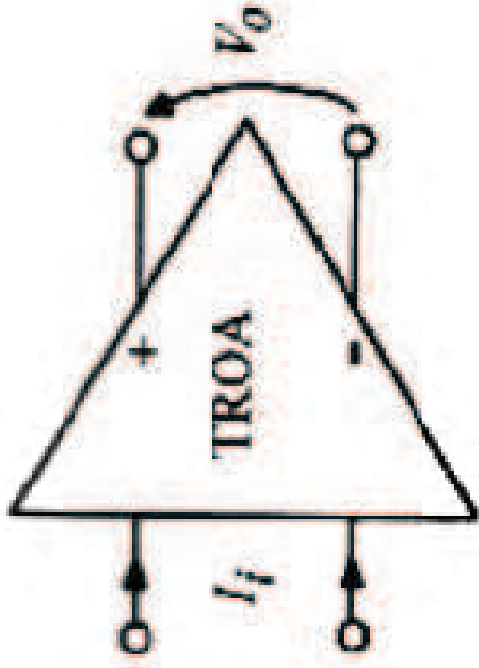
TRANSCONDUCTANCE OPAMP (OTA)

Transconductance opamp (OTA): voltage controlled current source with infinite transconductance gain, input and output resistances[3].

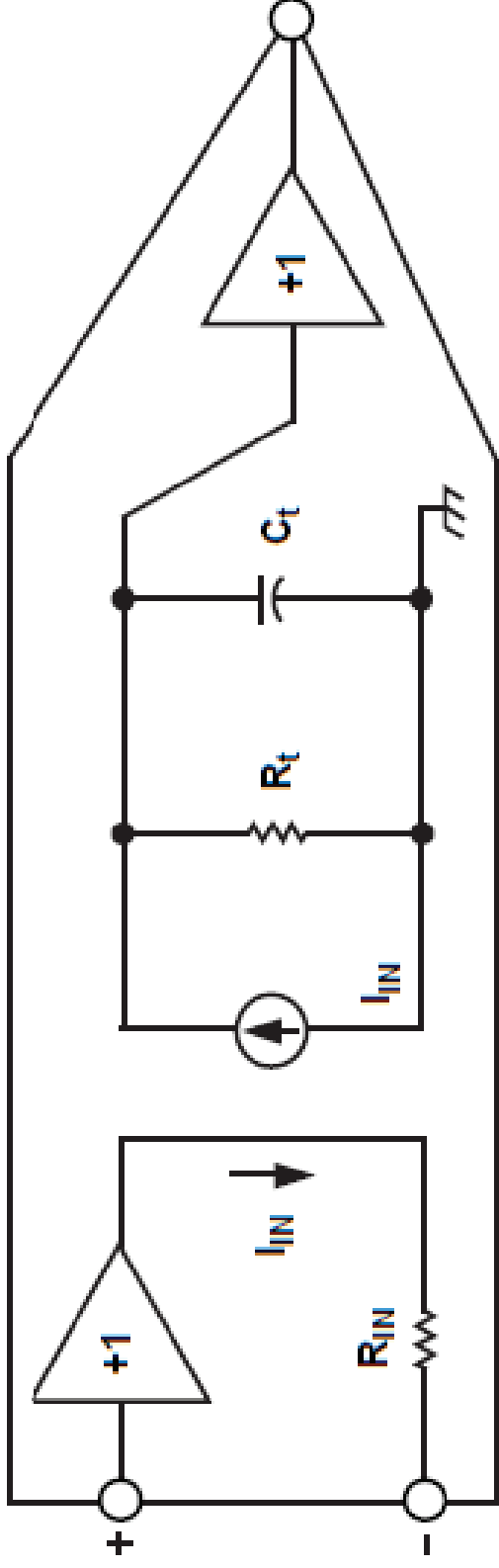


CURRENT FEEDBACK OPAMP (CFOA)

Current feedback opamp (CFOA): current controlled voltage source with infinite transresistance gain and zero input and output resistances.

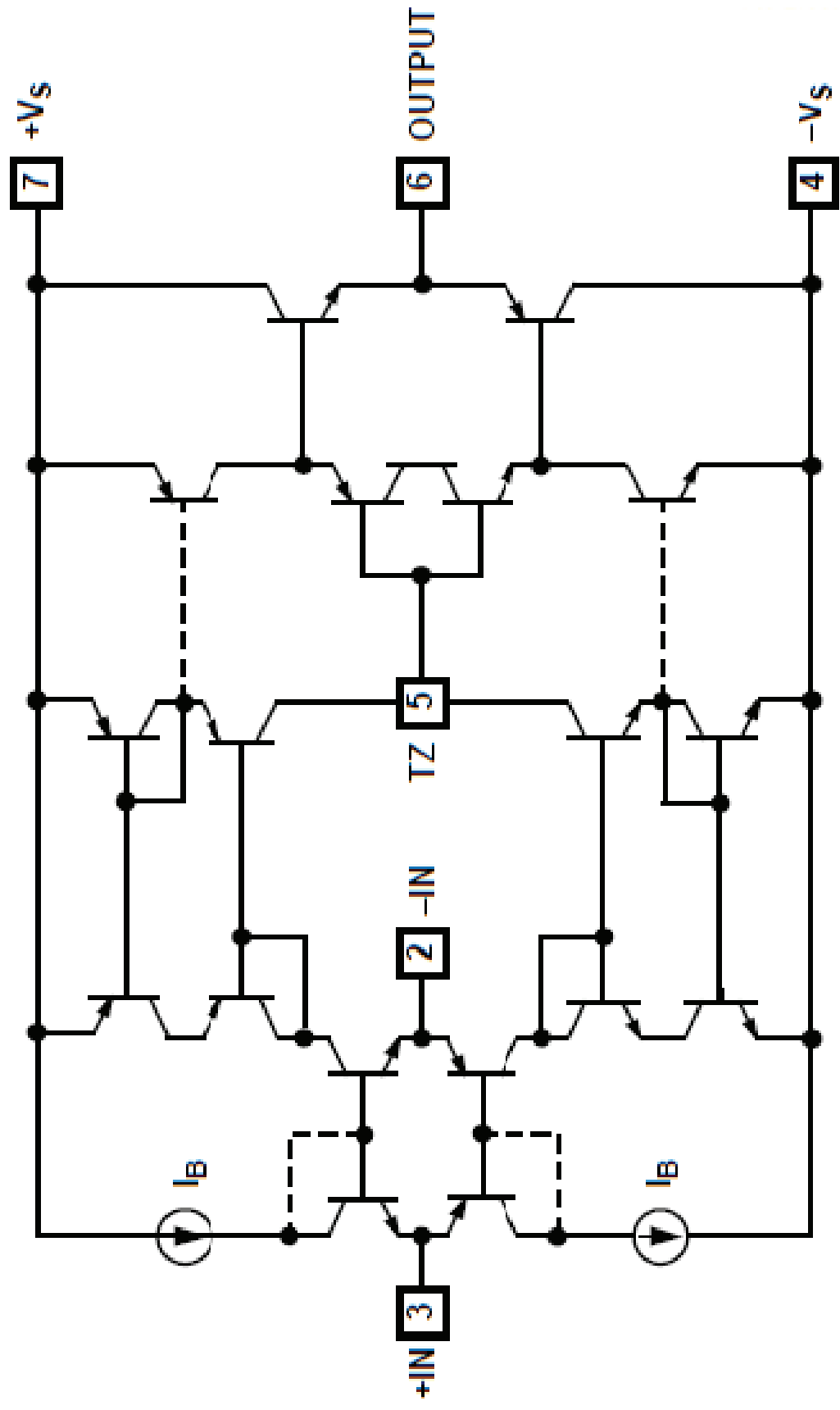


AD844 CFOA



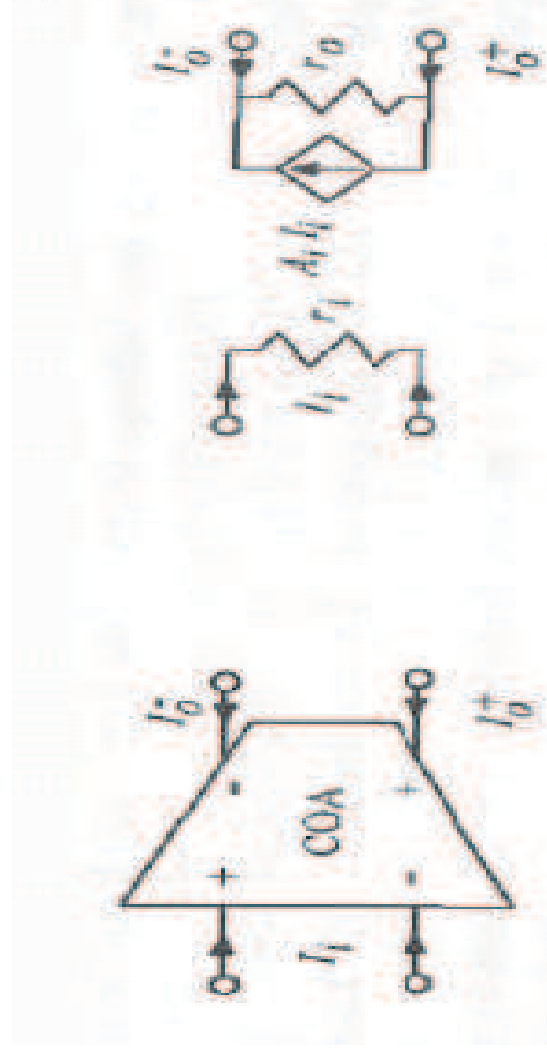
AD844 is a current feedback opamp. A CCII input stage is followed by a unity gain buffer which is the output stage[4].

AD844 CFOA



CURRENT OPAMP (COA)

Current opamp (COA): current controlled current source with infinite current gain and output resistance, zero input resistance[3].



$$\begin{bmatrix} V_{IN+} \\ V_{IN-} \\ I_{O+} \\ I_{O-} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ K & -K & 0 & 0 \\ -K & K & 0 & 0 \end{bmatrix} \begin{bmatrix} I_{IN+} \\ I_{IN-} \\ V_{O+} \\ V_{O-} \end{bmatrix}$$

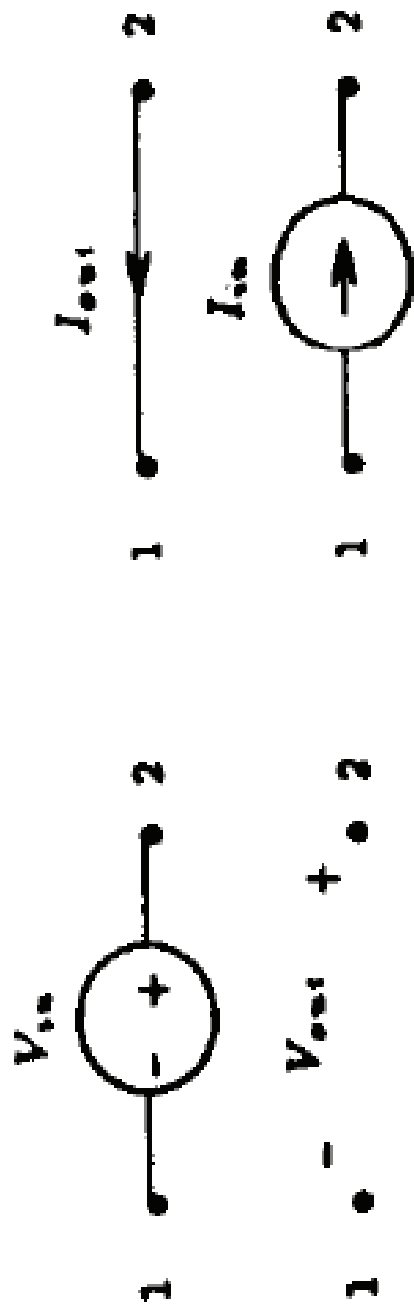
CURRENT OPAMP (COA)

The main advantage of using COA is its ability to replace with the voltage opamp when applying the adjoint network theorem in voltage mode to current mode transformation[5].

THE ADJOINT NETWORK THEOREM

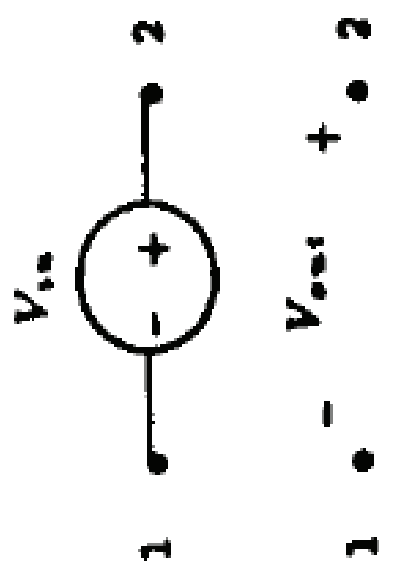
Creating an alternative realization of a linear network having the same transfer function can be performed using the principle of adjoint networks[6]. This principle forms the basis of current-based circuits. A voltage mode circuit can be transformed to a current mode one by using the adjoint elements [7].

Adjoint

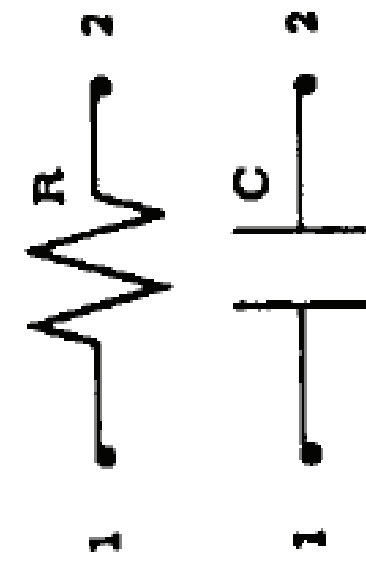
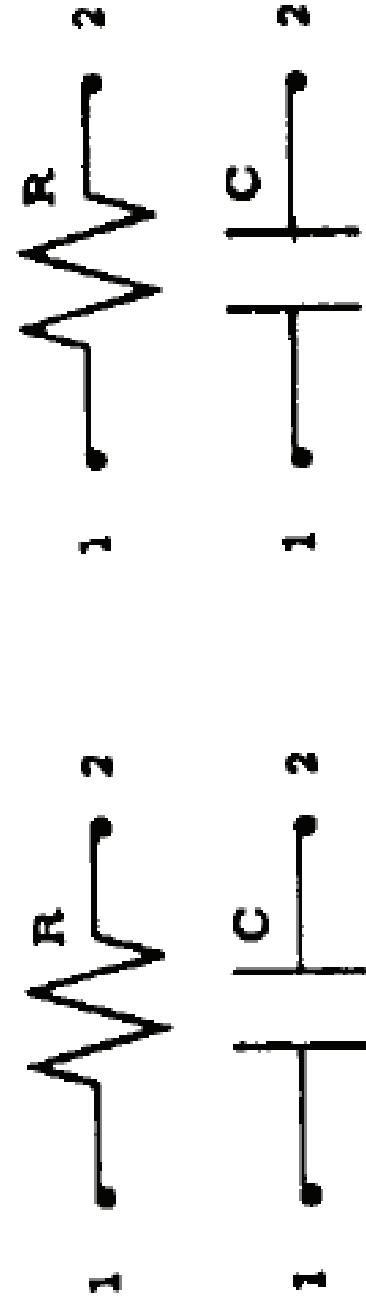


Signal Sources

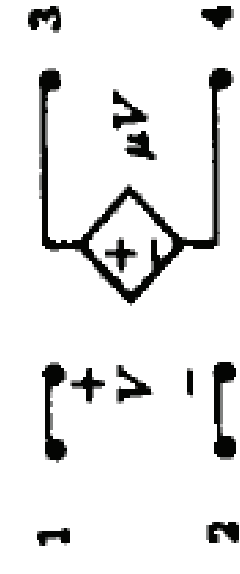
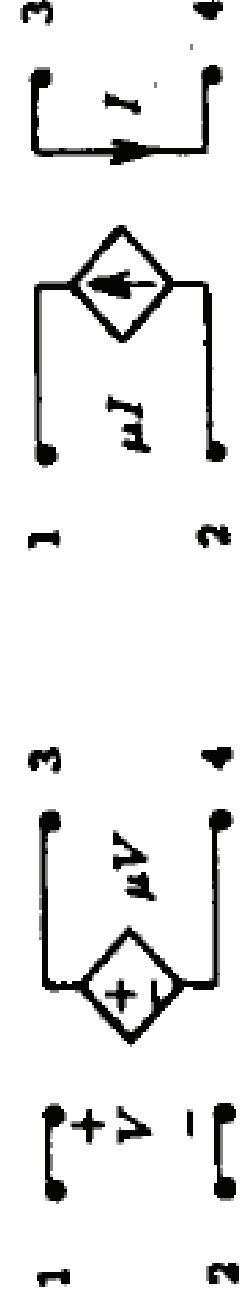
Element



Passive Elements



Controlled Sources



THE ADJOINT NETWORK THEOREM

We can describe the adjoint transformation procedure as follows: **Replace the input voltage source by a short circuit** and call the current flowing through it the new output response variable.

Next connect a current source to the output port of the original circuit. This will be the new input and the transfer function of this “adjoint circuit” will be the ratio of two currents[5].

THE ADJOINT NETWORK THEOREM

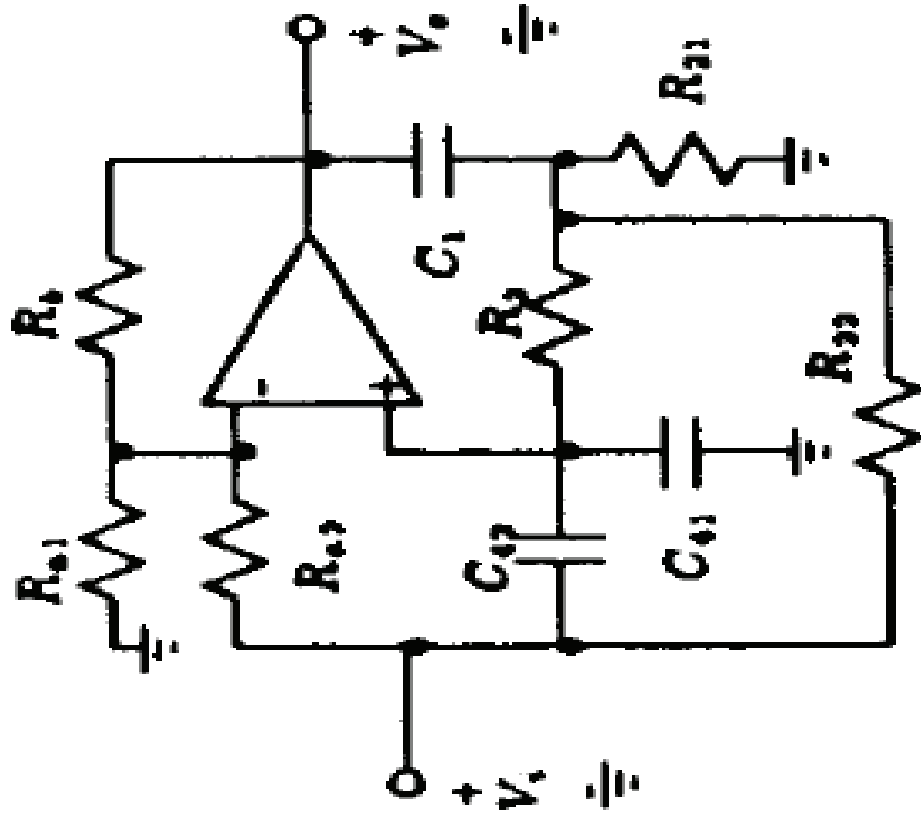
Finally replace each voltage-controlled voltage source (VCVS) by a current-controlled current source (CCCS). The input terminals of the CCCS should be connected to the output port of the VCVS and conversely, the output port of the CCCS is connected to the input port of the VCVS[5].

THE ADJOINT NETWORK THEOREM

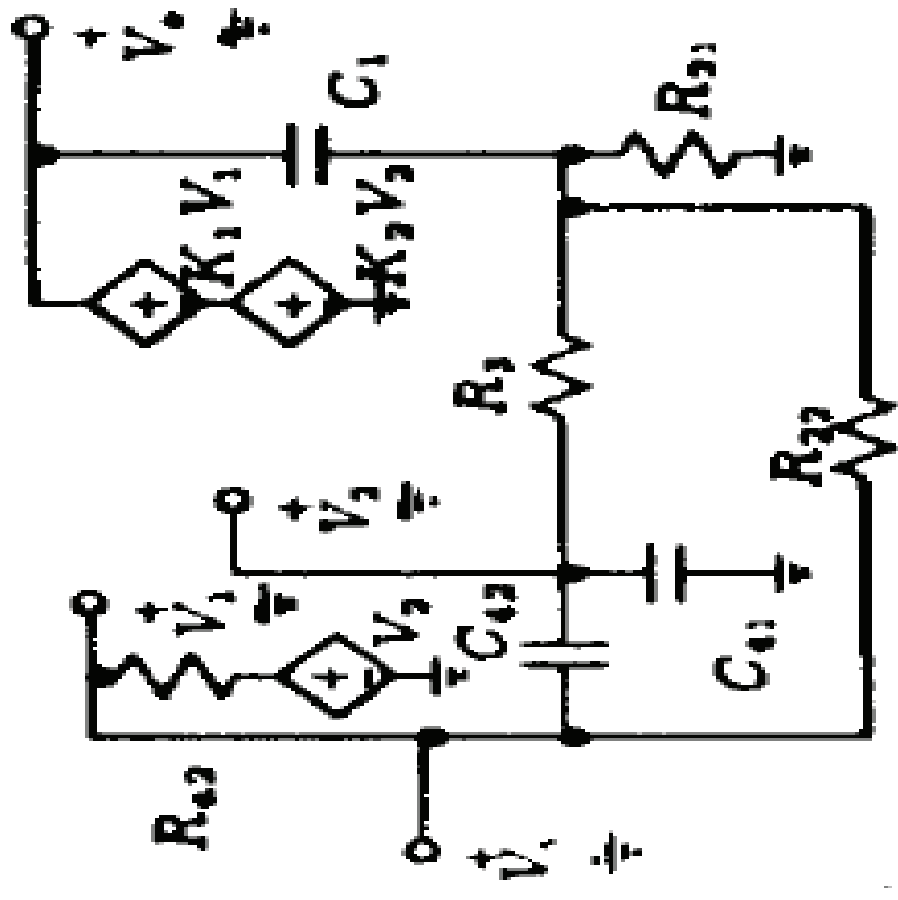
The resistors and capacitors are left unchanged. The result is an alternate circuit with the exact same transfer function[5].

Moreover two circuits have identical sensitivities to component variations[7].

ADJOINT NETWORK EXAMPLE

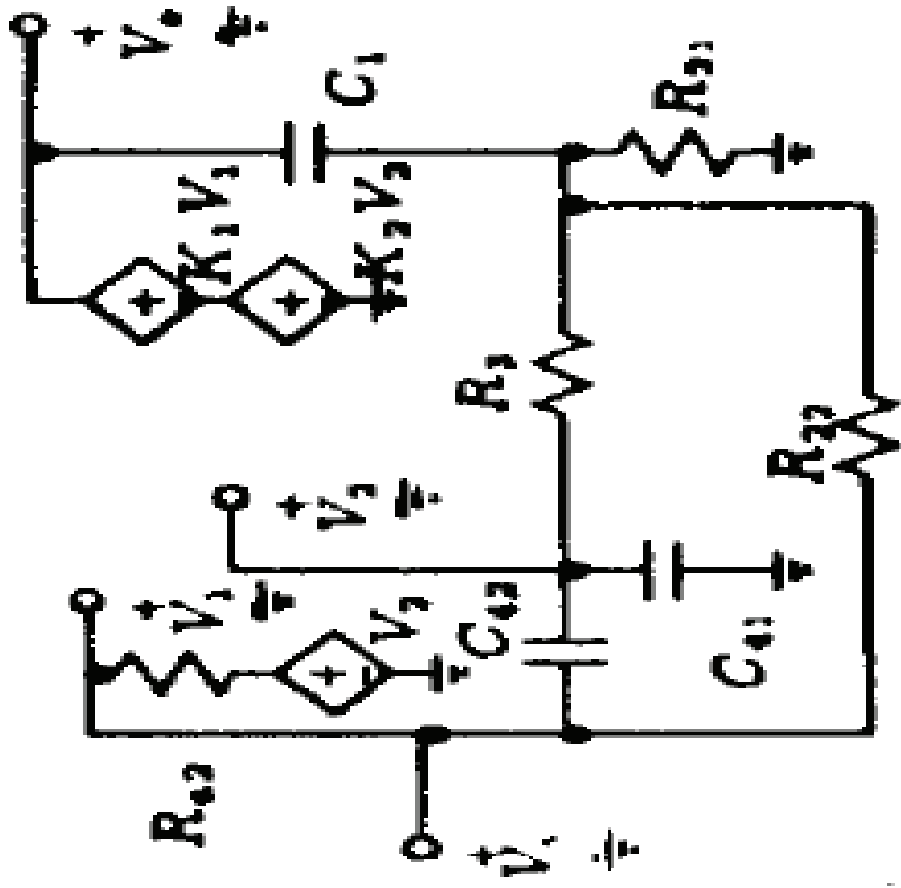


Low pass biquad (LPB)

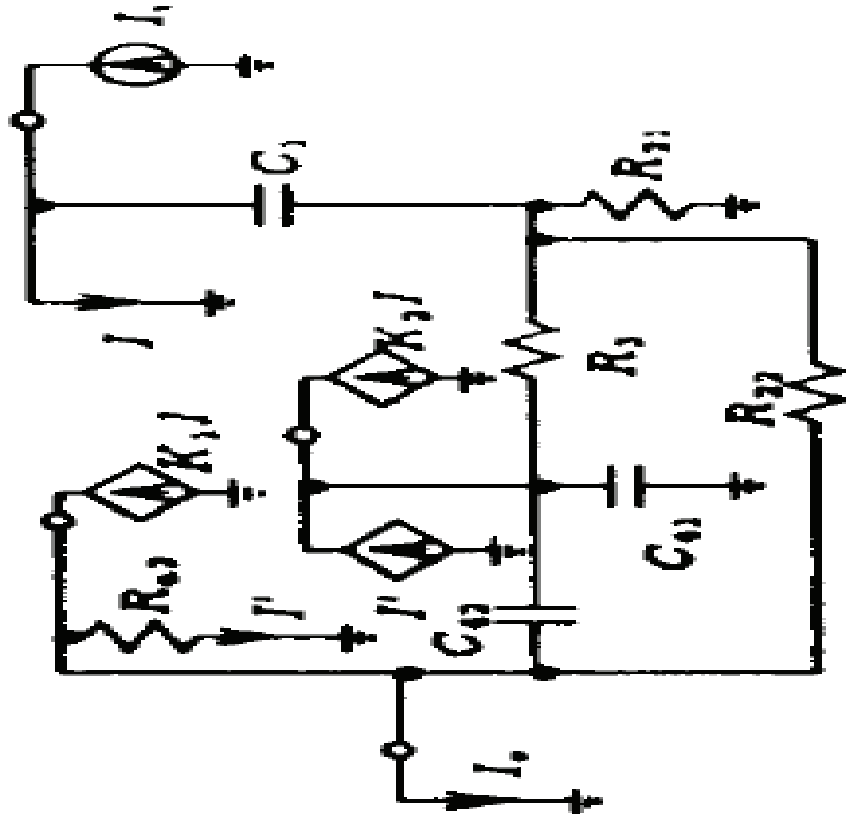


Equivalent VCVS representation

ADJOINT NETWORK EXAMPLE



Equivalent VCVS representation



Corresponding current-mode adjoint circuit

CURRENT OPAMP (COA)

It is not difficult to increase output resistance and current gain of a COA to a reasonable value. However, achieving a very low input resistance is a problem[8].

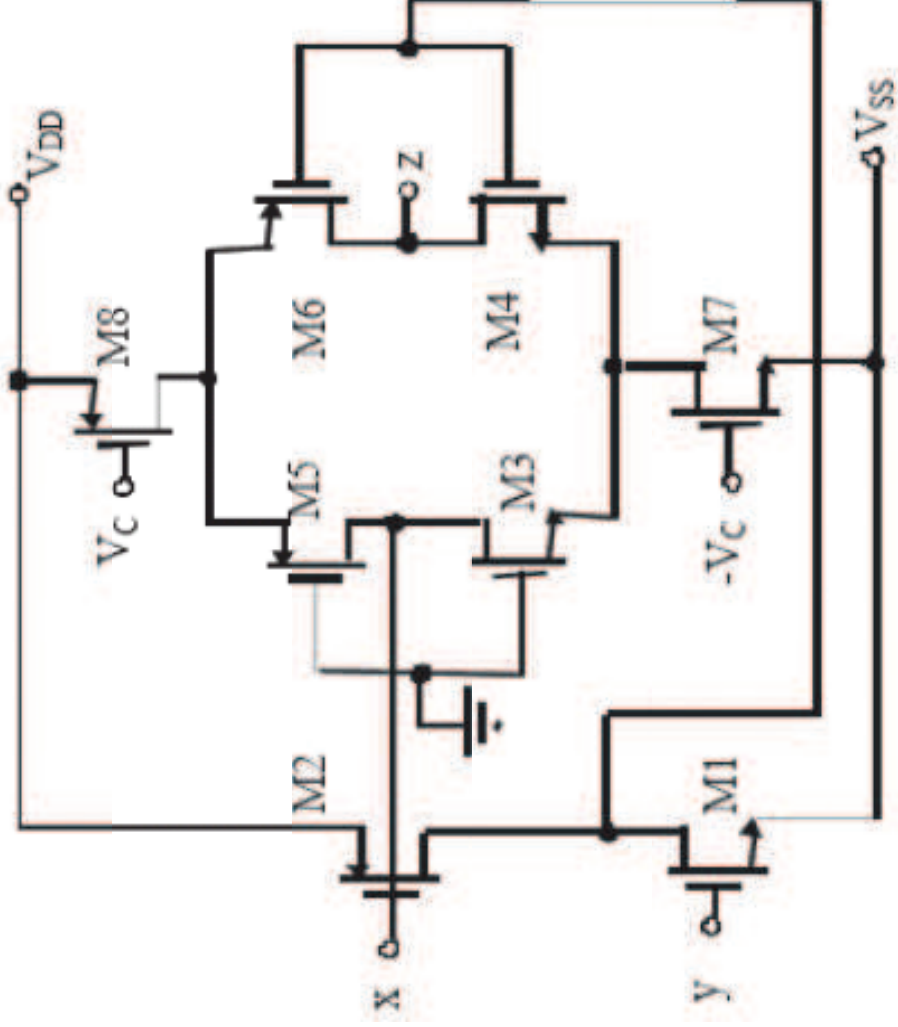
Some complicated negative feedback configurations have been suggested in [9,10] but it generally worsens the frequency response.

Positive feedback is another solution to lower the input resistance[8,11,12].

ARBEL GOLDMINZ OUTPUT STAGE

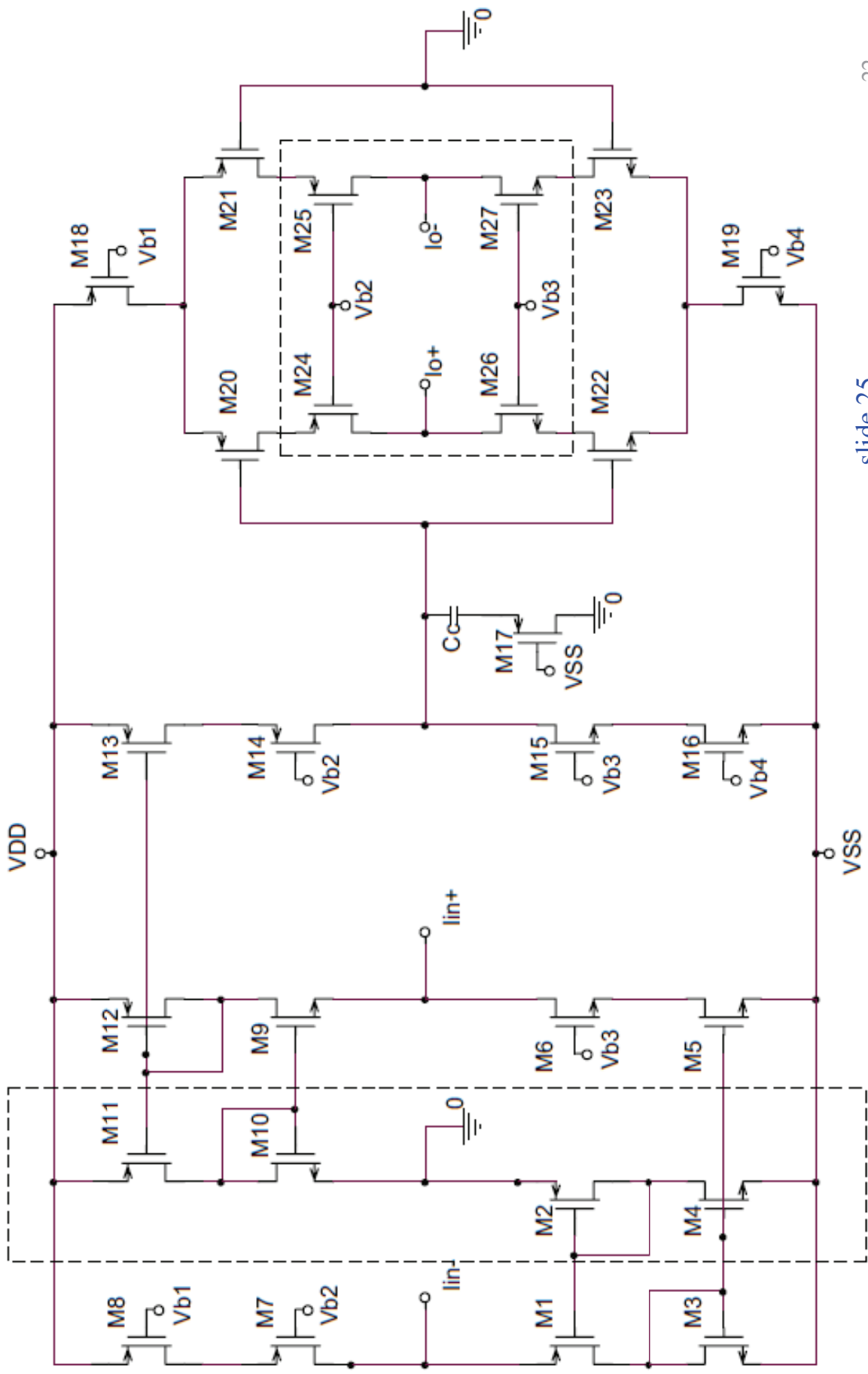
Arbel Goldminz output stage, which is the conventional current output stage, suffers from its output resistance[13]. An improved version of this stage is discussed in [8].

ARBEL GOLDMINZ OUTPUT STAGE



$$f_{\text{out}} \cong \left[\left(\frac{g_{m20}g_{ds21}}{g_{m21} + g_{m20}} \right) + \left(\frac{g_{m22}g_{ds23}}{g_{m23} + g_{m22}} \right) \right]^{-1}$$

PROPOSED COA



PROPOSED COA

Transistors	$W(\mu\text{m})/L(\mu\text{m})$
M1, M2	20/0.7
M3, M4, M5, M6	20/1
M7	10/0.7
M8	27/0.7
M9, M10	15/0.7
M11, M12, M13	20/1.4
M14	40/1.4
M15	20/0.7
M16	17.8/1.4
M17	12.5/1
M18	118/1
M19	47/1
M20, M21	75/1
M22, M23	40/0.7
M24, M25	100/1
M26, M27	60/0.7

Parameter	Value
$V_{\text{DD}} - V_{\text{SS}}$	$\pm 1.5\text{ V}$
$V_{\text{b1}}, V_{\text{b2}}$	$0.6\text{ V}, -0.3\text{ V}$
$V_{\text{b3}}, V_{\text{b4}}$	$0.3\text{ V}, -0.8\text{ V}$
$I_{\text{D8}}, I_{\text{D16}}$	$30\ \mu\text{A}$
$I_{\text{D18}}, I_{\text{D19}}$	$100\ \mu\text{A}$

PROPOSED COA

The amplifier is configured from a differential input transimpedance stage followed by a differential output transconductance stage. Shown in dashed lines at the input stage, M2, M4 and M10, M11 compose positive feedback loops to reduce positive and negative input resistances, respectively[8]. The frequency response is not noteworthy affected since only four extra transistors are added.

PROPOSED COA

If no positive feedback is applied to the inputs, the input resistances will be as shown:

$$r_{in-} \approx \frac{1}{g_{m1}}$$

$$r_{in+} \approx \frac{1}{g_{m9}}$$

And generally these values are not low enough.

PROPOSED COA

However, if some positive feedback is applied to the inputs, the input resistances will change as shown in [8]:

$$r_{in-} \cong \frac{1}{g_{m1}g_{m3}} \left[(g_{ds1} + g_{m3} + g_{ds3}) - \frac{g_{m1}g_{m4}}{g_{ds4} + g_{m2} + g_{ds2}} \right],$$
$$r_{in+} \cong \frac{1}{g_{m9}g_{m12}} \left[(g_{ds9} + g_{m12} + g_{ds12}) - \frac{g_{m9}g_{m11}}{g_{ds11} + g_{m10} + g_{ds10}} \right].$$

PROPOSED COA

The second terms mainly affect input resistance value . That is, if it is selected close to zero, r_{in} also goes near zero[8].

$$r_{in-} \cong \frac{1}{g_{m1}g_{m3}} \left[\frac{g_{ds1} + g_{m3} + g_{ds3}}{g_{ds4} + g_{m2} + g_{ds2}} - \frac{g_{m1}g_{m4}}{g_{ds9} + g_{m12} + g_{ds12}} \right]$$
$$r_{in+} \cong \frac{1}{g_{m9}g_{m12}} \left[\frac{g_{m9}g_{m11}}{g_{ds11} + g_{m10} + g_{ds10}} \right].$$

PROPOSED COA

Furthermore, these values must be always chosen as positive. Otherwise, because of the negative input resistances, the problem of stability will occur. To overcome that problem, the values can be chosen as follows, as suggested in [8].

$$g_{m3} = g_{m4}, g_{m1} = g_{m2}, g_{m11} = g_{m12}, g_{m9} = g_{m10}$$

Selecting W/L ratios equal will make g_m s equal.

PROPOSED COA

Output resistance of traditional current output stage (Arbel Goldminz) is:

$$r_{\text{out}+} = r_{\text{out}-} \cong \left[\left(\frac{g_{m20}g_{ds21}}{g_{m21} + g_{m20}} \right) + \left(\frac{g_{m22}g_{ds23}}{g_{m23} + g_{m22}} \right) \right]^{-1}$$

M24, M25, M26 and M27 in dashed line are added to the conventional current output stage for getting very big output resistance values[8].

PROPOSED COA

The output resistance of the proposed COA is:

$$r_{\text{out}+} = r_{\text{out}-} \cong \left[\left(\frac{g_{m20}g_{ds21}g_{ds25}}{g_{m25}(g_{m21} + g_{m20})} \right) + \left(\frac{g_{m22}g_{ds23}g_{ds27}}{g_{m27}(g_{m23} + g_{m22})} \right) \right]^{-1}$$

It is approximately $g_m r_o$ times bigger than the traditional Arbel Goldminz output stage[8].

PROPOSED COA

M_{17} and C_C are used for compensation. M_{17} forms a resistor and improves the frequency response of the COA. The DC current gain and the gain-bandwidth product are given as follows[8]:

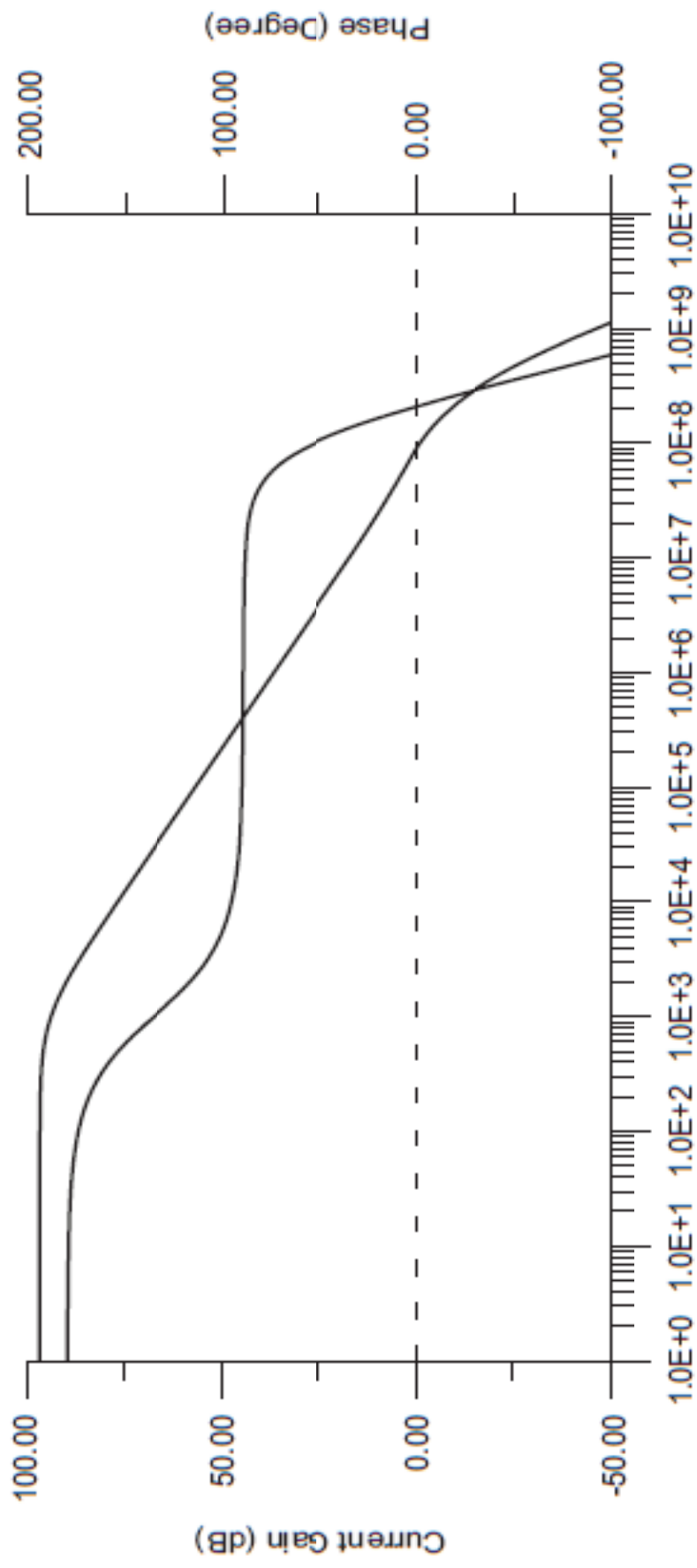
$$A_i(0) \cong \frac{g_{m20}g_{m22}}{2} \left[\frac{g_{ds14}g_{ds13}}{g_{m14}} + \frac{g_{ds15}g_{ds16}}{g_{m15}} \right]^{-1},$$

$$f_{GBW} \cong \frac{1}{2\pi} \frac{g_{m20} + g_{m22}}{2C_C}.$$

PERFORMANCE OF THE PROPOSED COA

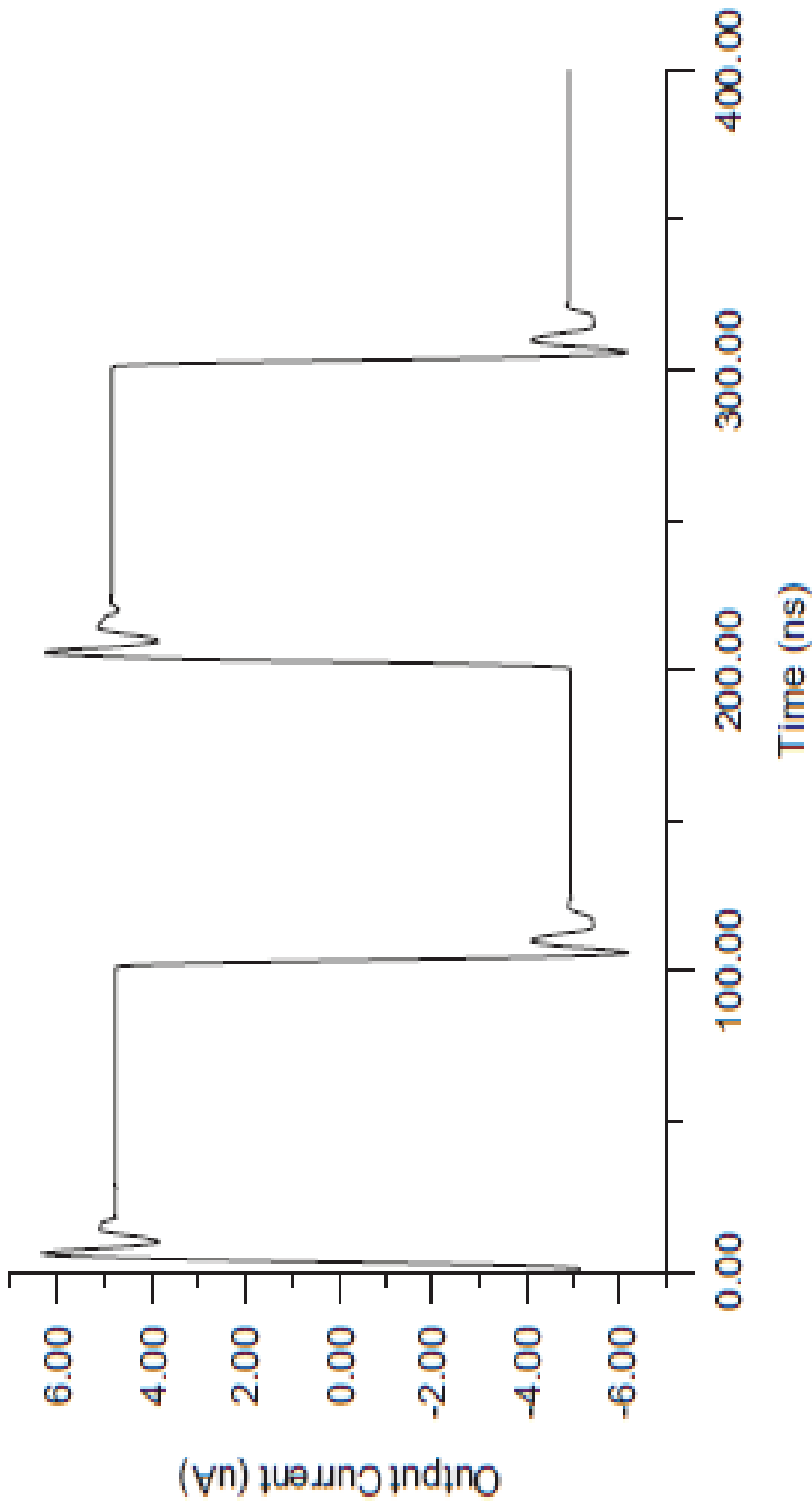
Parameter	Value
Power dissipation	0.66 mW
Open-loop gain	96 dB
GBW	92 MHz
Phase margin ($C_c = 1.2$ p $R_c = 2.4$ k Ω)	60°
Output voltage range	± 0.6 V
Slew rate	4 μ A/ns
Input resistance (n)	124 Ω
Input resistance (p)	109 Ω
Output resistance	30 M Ω
Input voltage offset (n)	≈ 1.6 mV
Input voltage offset (p)	≈ -3.5 mV

PERFORMANCE OF THE PROPOSED COA



Open-loop frequency response of the COA.

PERFORMANCE OF THE PROPOSED COA



Response of the COA in unity-gain feedback to a $\pm 5\mu\text{A}$ input step ($f = 5\text{ MHz}$).

PERFORMANCE OF THE PROPOSED COA

- ✓ 0.35 μ model parameters was used in spice simulations.
- ✓ Threshold voltages of the mosfets are nearly 0.5V and -0.7V for NMOS and PMOS respectively.
- ✓ As a result of the class A operation, speed of the COA is limited by quiescent current.

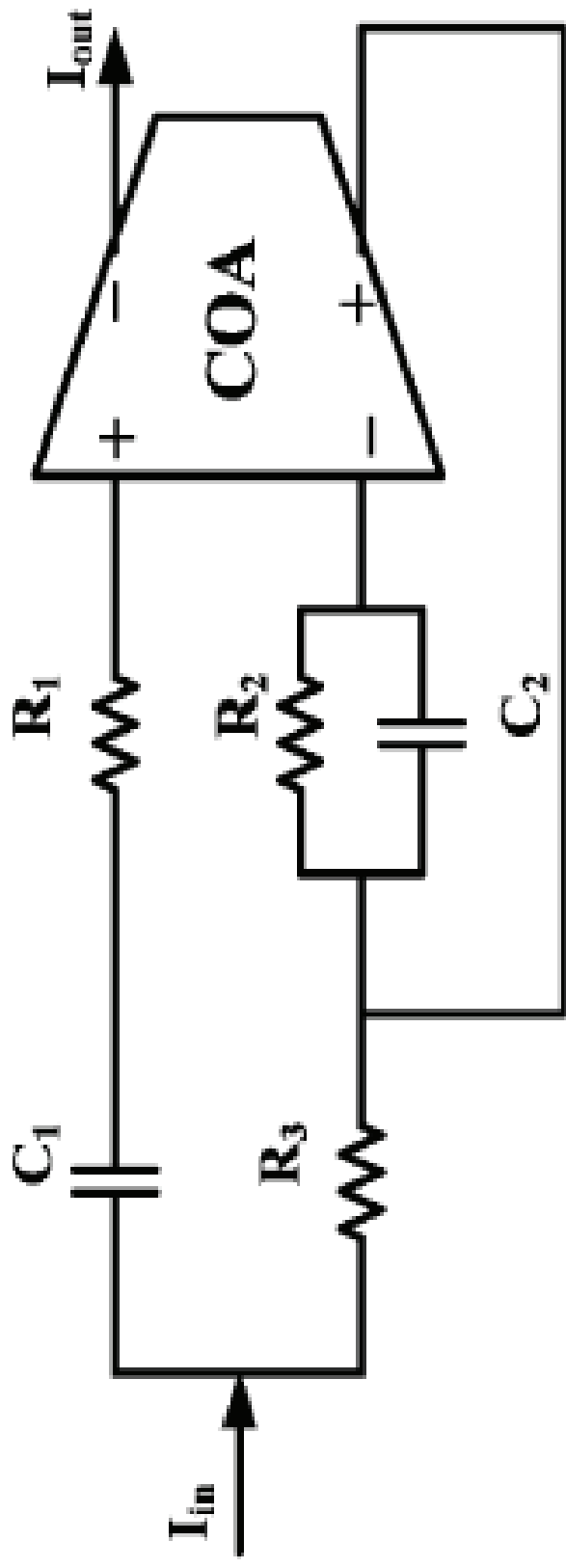
COA BASED FILTERS

Two second-order low-pass (LP) and high pass (HP) filter configurations are proposed. Single COA is used for each realization and matching conditions are:

$$R_1 = R_3 = R_{LP}, \quad C_1 = C_2 = C_{LP} \text{ for LP filter,}$$

$$R_4 = R_5 = R_{HP}, \quad C_4 = C_6 = C_{HP} \text{ for HP filter.}$$

COA BASED LOW-PASS FILTER



$$\frac{i_{\text{out-LP}}}{i_{\text{in}}} = \frac{1}{s^2 + s \frac{1}{R_2 C_{LP}} + \frac{1}{2R_{LP} R_2 C_{LP}^2}}$$

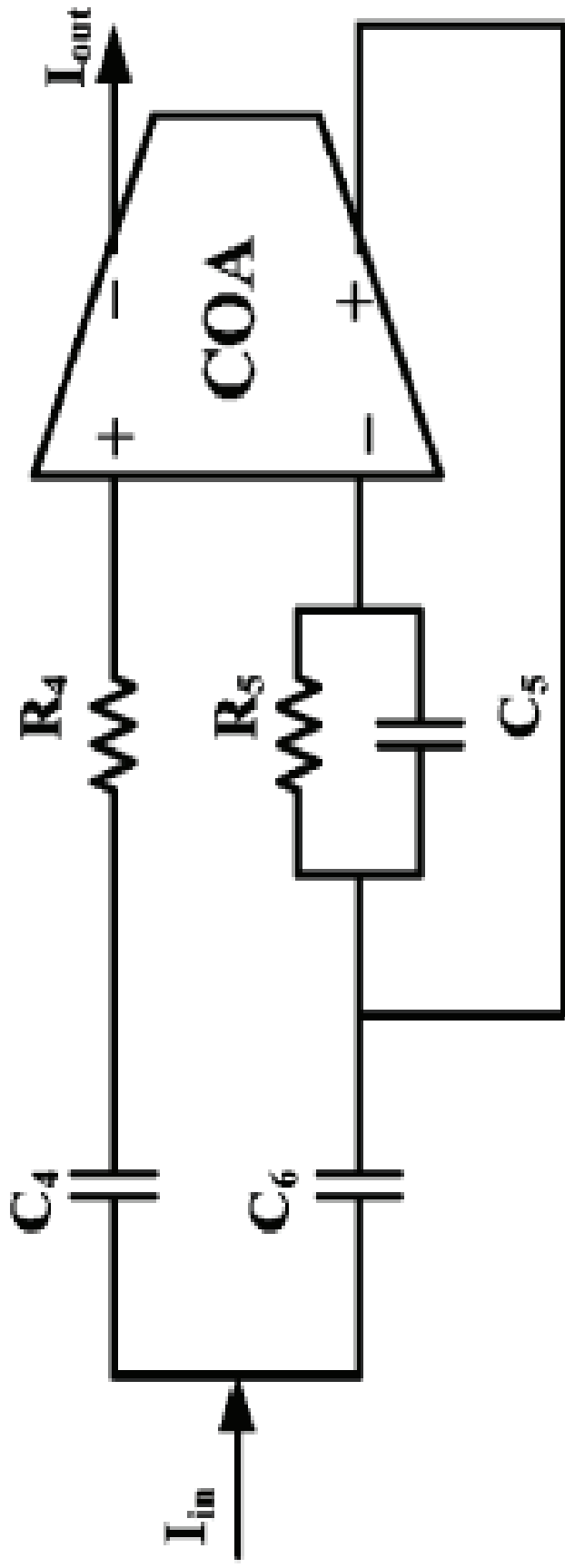
COA BASED LOW-PASS FILTER

$$\frac{i_{\text{out-LP}}}{i_{\text{in}}} = \frac{\frac{1}{2R_{\text{LP}}R_2C_{\text{LP}}^2}}{s^2 + s\frac{1}{R_2C_{\text{LP}}} + \frac{1}{2R_{\text{LP}}R_2C_{\text{LP}}^2}}.$$

$$\omega_0 = \sqrt{\frac{1}{2R_{\text{LP}}R_2C_{\text{LP}}^2}}, \quad Q = \sqrt{\frac{R_2}{2R_{\text{LP}}}}.$$

$$S_{R_{\text{LP}}}^{\omega_0} = S_{R_2}^{\omega_0} = -1/2, S_{C_{\text{LP}}}^{\omega_0} = -1, S_{R_2}^Q = 1/2, S_{R_{\text{LP}}}^Q = -1/2,$$

COA BASED HIGH-PASS FILTER



$$\frac{i_{out-HP}}{i_{in}} = \frac{s^2}{s^2 + s \frac{1}{R_{HP}C_{HP}} + \frac{2}{R_{HP}^2 C_{HP} C_5}}$$

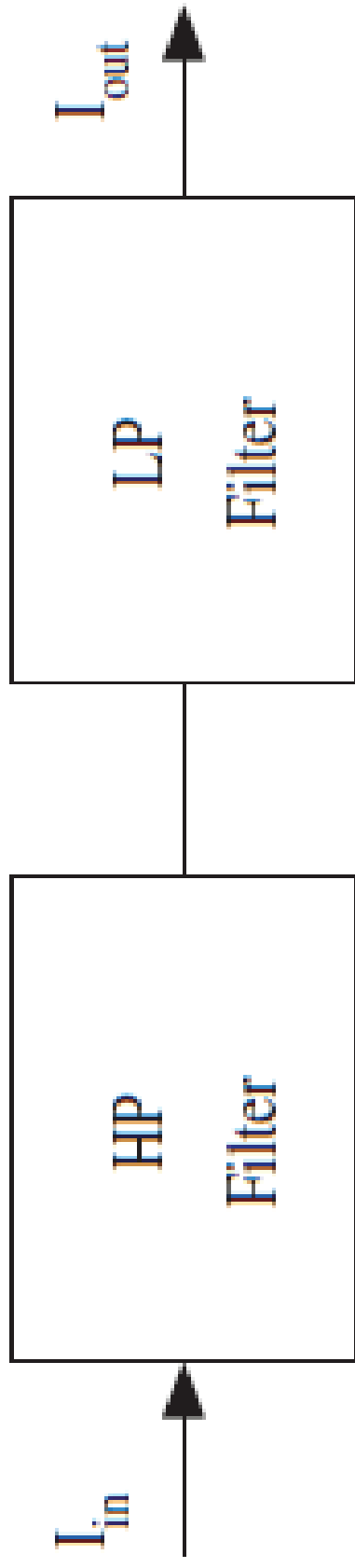
COA BASED HIGH-PASS FILTER

$$\frac{\dot{i}_{\text{out-HP}}}{\dot{i}_{\text{in}}} = \frac{s^2}{s^2 + s \frac{1}{R_{\text{HP}} C_{\text{HP}}} + \frac{2}{R_{\text{HP}}^2 C_{\text{HP}} C_5}}$$

$$\omega_0 = \sqrt{\frac{2}{R_{\text{HP}}^2 C_{\text{HP}} C_5}}, \quad Q = \sqrt{\frac{C_{\text{HP}}}{2C_5}}$$

$$S_{C_5}^{u_0} = S_{C_5}^{u_0} = -1/2, S_{R_{\text{HP}}}^{u_0} = -1, S_{C_{\text{HP}}}^{u_0} = 1/2, S_{C_5}^{u_0} = -1/2,$$

COA BASED BAND-PASS FILTER



$$\frac{i_{out}}{i_{in}} = \frac{\frac{1}{2R_{LP}R_2C_{LP}^2}}{s^2 + s\frac{1}{R_2C_{LP}} + \frac{1}{2R_{LP}R_2C_{LP}^2}} \cdot \frac{s^2}{s^2 + s\frac{1}{R_{HP}C_{HP}} + \frac{2}{R_{HP}^2C_{HP}C_5}}$$

COA BASED BAND-PASS FILTER

For $C_{LP}=7.5\text{pF}$, $R_{LP}=R_2=5\text{k}\Omega$

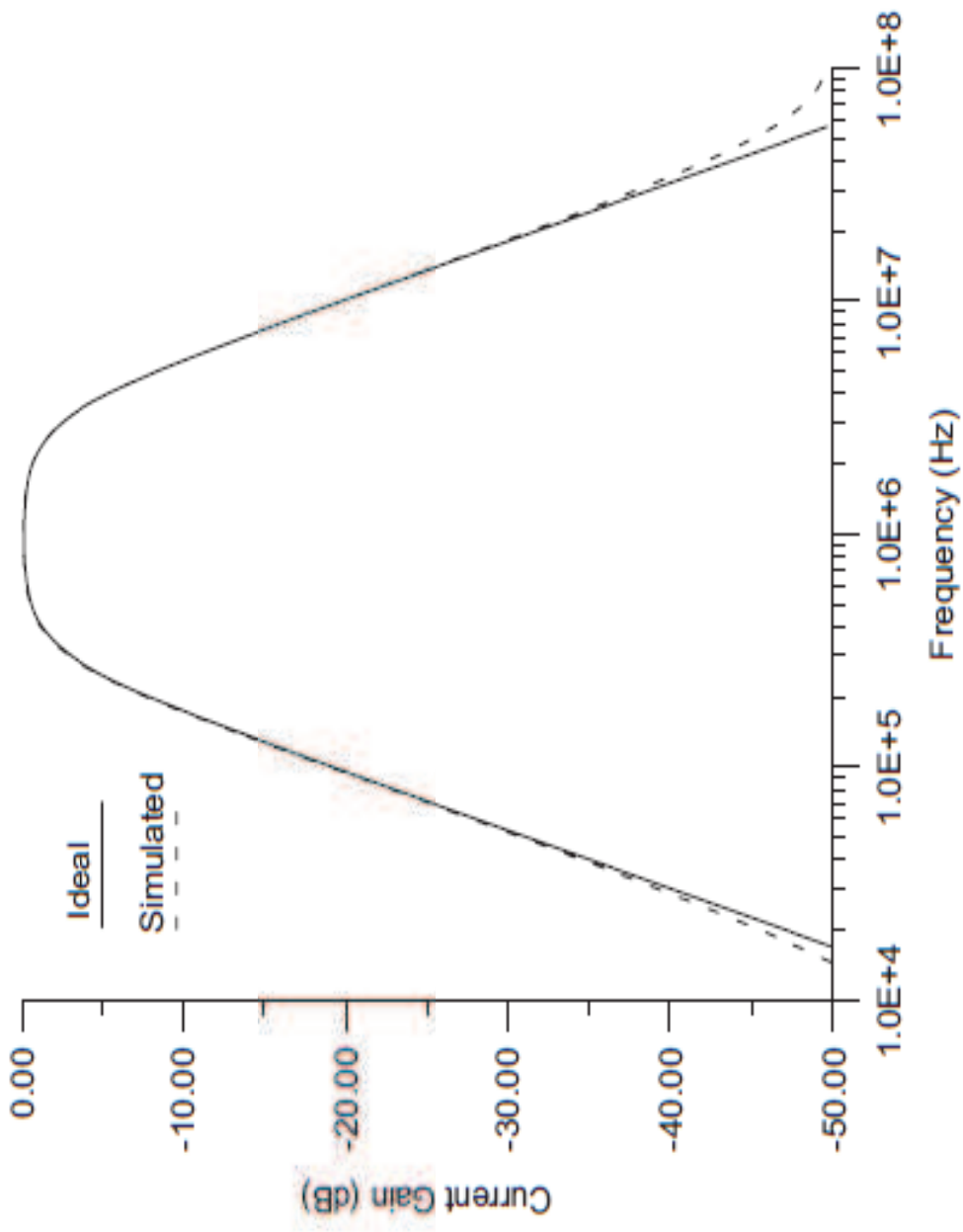
$f_{0LP} \approx 3.0\text{MHz}$, $Q = 0.707$

For $C_{HP}=C_5=15\text{pF}$, $R_{HP}=50\text{k}\Omega$

$f_{0HP} \approx 300.1\text{ kHz}$, $Q = 0.707$

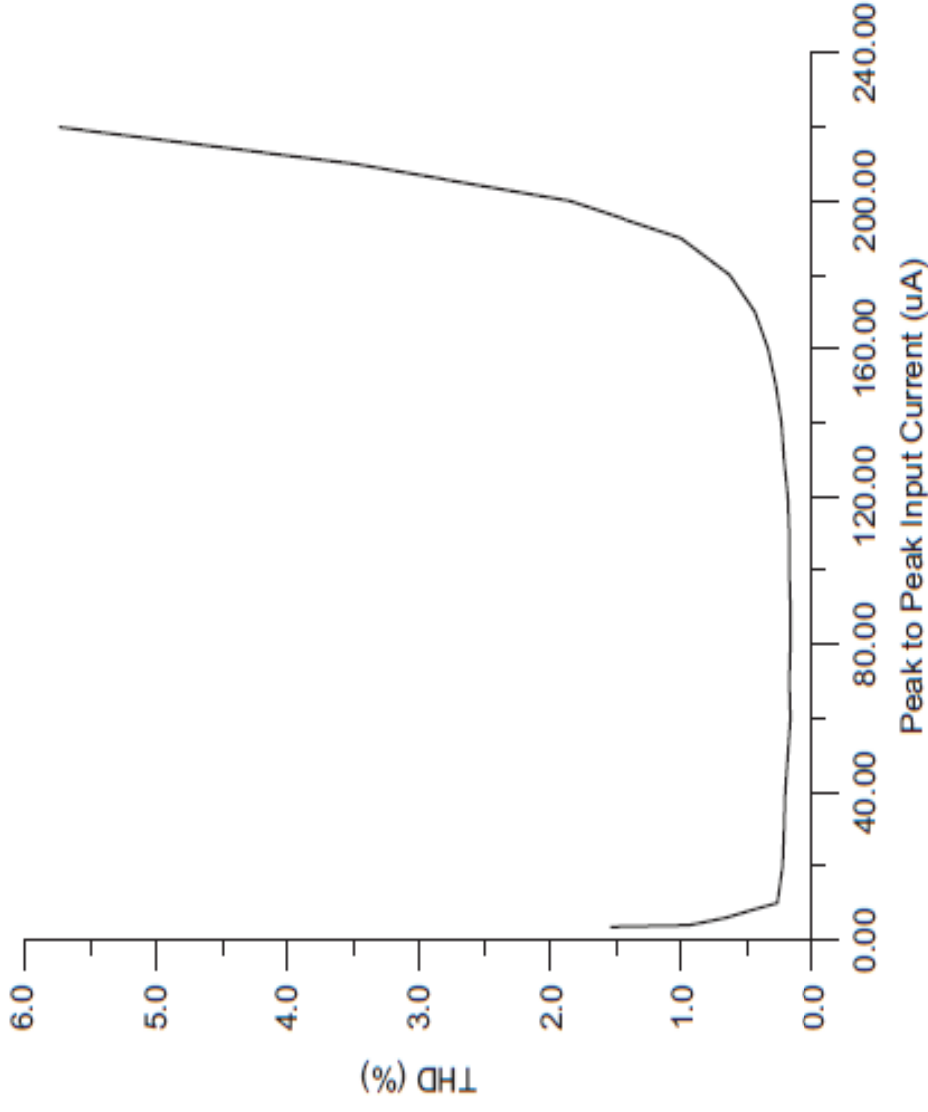
So $f_{0BP} \approx 1\text{MHz}$, $Q = 0.707$ (flat response)

COA BASED BAND-PASS FILTER



Simulated and ideal band-pass filter responses.

COA BASED BAND-PASS FILTER



Total harmonic distortion (THD) values of the filter versus input peak to peak current at 1 MHz frequency.

CONCLUSION

A very accurate, fully differential COA is proposed in [8]. A novel approach is used in input resistance improvement and also very high output resistance is achieved by modifying traditional current output stage. Due to the simple circuitry, 92MHz GBW is obtained. Moreover, a very high DC gain and $\pm 0.6V$ output voltage swing are obtained. A new COA-based fourth-order BP filter is proposed as an application. Simulation results are the evidences of accuracy of the proposed fully differential COA.

References:

- [1] G. Palmisano, G. Palumbo, S. Pennisi, “CMOS current amplifiers”, Kluwer Academic Publishers, 1999
- [2] C. Toumazou, F.J. Lidgey, D.G. Haigh (ed.), “Analog IC design: the current-mode approach”, Peter Peregrinus Ltd., 1998.
- [3] H. Kuntman, “Current Operational Amplifiers”, presentation, 2009
- [4] Analog Devices, “60 MHz 2000 V/ μ s Monolithic Op Amp AD844” datasheet, 2009
- [5] Roberts GW, Sedra AS. “A general class of current amplifier based biquadratic. filter circuits”, IEEE Trans Circuits Syst 1992;39:257–63.
- [6] Roberts GW, Sedra AS. ”All current-mode frequency selective circuits”, Electronics Letters, 1989
- [7] GC Temes, JW LaPatra, “Introduction to circuit synthesis and design”, McGraw-Hill, 1977

References:

- [8] M. Altun and H. Kuntman, ‘Design of a Fully Differential Current Mode Operational Amplifier with Improved Input-Output Impedances and Its Filter Applications’, AEU: International Journal of Electronics and Communications, Vol.62, No. 3, 239-244, 2008.
- [9] Surakamponorn W, Riewruja V, Kumwachara K, Dejhan K. Accurate CMOS-based current conveyors. IEEE Trans Instrum Meas 1992;40:699–702.
- [10] Palmisano G, Palumbo G. A simple CMOS CCII+. Int J Circuit Theory Appl 1995;23(6):599–603.
- [11] Wang W. Wideband class AB (push–pull) current amplifier in CMOS technology. Electron Lett 1990;26(8):543–5.
- [12] Altun M, Kuntman H. A wideband CMOS current-mode operational amplifier and its use for band-pass filter realization. In: Proceedings of applied electronics, Pilsen, 2006, p. 3–6.

References:

- [13] Arbel AF, Goldminz L. Output stage for current-mode feedback amplifiers, theory and applications. *Analog Integrated Circuits Signal Process* 1992;2:243–55.



Thanks...