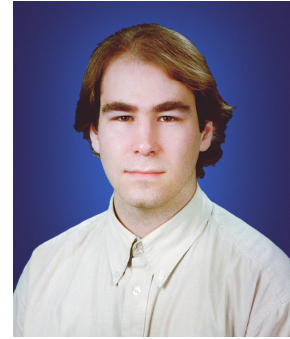


CURRICULUM VITAE

Devrim Yılmaz AKSIN



Office Address

Address : Istanbul Technical University
Dept. of Electronics and Communication Engineering.
Maslak, Istanbul, Turkey
E-Mail : devrimaksin@ieee.org

Education

1. Ph.D. Electronics (continuing) at University of Texas at Dallas, from 2002 - 2006
2. Ph.D. Electronics at Texas A&M University, from 2001 to 2002
3. Ph.D. Electronics and Communication Engineering, at Istanbul Technical University (ITU). From 1999 to 2000
4. M.Sc. Electronics and Communication Engineering, at ITU. From 1996 to 1999.
5. B.Sc. Electronics and Communication Engineering, at ITU. From 1992 to 1996.

Work Experience

Analog design engineer and project leader at Texas Instruments Incorporated from 2004 – 2007 Dallas TX USA with CMS (custom mixed signal) HVAL (High Volume Analog and Logic) business. Worked on high performance data converters, power management ICs.

ASIC Design Engineer and ASIC Project Manager at ITU - ETA ASIC Design Center, Istanbul, TURKEY. From 01.01.1996 through 01.01.2001.

Teaching Experience

1. Research Assistant at University of Texas at Dallas, from 2002 – 2004.
2. Research Assistant at Analog & Mixed Signal Center, Electrical Engineering Department, Texas A&M University. From 2001 - 2002
3. Research and Teaching Assistant at Electronics and Communication Engineering Department, Istanbul Technical University. From 1997 to 2001.

Theses

1. Design and Implementation of Second Order Sigma-Delta Analog to Digital Converter B.Sc. Thesis, 1996. Supervised by Dr. Duran Leblebici, ITU.
2. A Programmable CMOS Analog Vector Quantizer Chip, M.Sc. Thesis, 1999. Supervised by Dr. Uğur Çilingiroğlu, ITU.
3. 11-bits sub-ranging analog to digital converter and SSA tool, PhD Thesis 2006. Supervised by Dr. Franco Maloberti, UTD.

Conference Presentations

1. Aksin, D.Y. Al-Shyoukh, M. Maloberti, F. "An 11 Bit Sub-Ranging SAR ADC with Input Signal Range of Twice Supply Voltage", Circuits and Systems, 2007, ISCAS 2007, IEEE International Symposium on, May 2007 (accepted for publication).
2. Aksin, D.Y. Al-Shyoukh, M. Maloberti, F. "A Bootstrapped Switch for Precise Sampling of Inputs with Signal Range Beyond Supply Voltage", Custom Integrated Circuits Conference, 2005, Proceeding of the IEEE, pp 743-746, Sept. 2005.
3. Aksin, D.Y. Gregori, S. Maloberti, F. "High-Efficiency Power Amplifier for Wireless Sensor Networks", Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on, Vol 6. pp 5898-5901, May 2005.
4. Aras, S Aksin, D.Y. "A compact Distance cell For Analog Classifiers", Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on, Vol 4. pp 3627-3630, May 2005.
5. Aksin, D.Y. Maloberti, F. "Symbolic small-signal Analysis (SSA) tool", Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on, Vol 3. pp3007-3010, May 2005.
6. Aksin, D.Y. Maloberti, F. "Non-Linear Behavioral Model of a Bipolar Track and Hold Amplifier For High-Speed and High-Resolution ADCs", Electronics, Circuits and Systems, 2005, ICECS 2005, IEEE International Symposium on, ICECS 2005, Dec. 2005
7. Aksin, D.Y. Maloberti, F. "AHDL Behavioral Model of Track and Hold Amplifiers For High-Speed High-Resolution ADC", IEEJ International Analog VLSI Workshop, 2004, AVLSIWS 2004, Oct. 2004.
8. Aksin, D.Y. Maloberti, F. "Very high-speed BJT Buffer for Track-and- Hold Amplifiers with Enhanced Linearity", Circuits and Systems, 2002, ISCAS 2002, IEEE International Symposium on, Vol 5. pp569-572, May 2002.
9. Aksin, D.Y. Aras, S. Goknar I.C. "A Compact Distance Cell for Analog Classifiers", Proceedings of the European Conference on Circuit Theory and Design, Espoo, Finland, Sept 2001
10. Aksin, D.Y. Aras, S. Goknar I.C. "CMOS Realization Of User Programmable, Single Level, Double-Threshold Generalized Perceptron", The Ninth Turkish Symposium on Artificial Intelligence and Neural Networks, Tainn 2000, June 2000.
11. Cilingiroglu, U. Aksin D.Y. "A 4-transistor Euclidean Distance cell for Analog Classifiers" Circuits and Systems, 1998, ISCAS 1998, IEEE International Symposium on, Vol 1. pp84-87, May 1998.

Journal Papers

1. Aksin, D.Y. Aras, S. Goknar I.C. "Realization and Extensions of User Programmable, Single-Level, Double- Threshold Generalized Perceptron", Istanbul Universitesi Muhendislik Fakultesi Elektrik Elektronik Dergisi, Cilt 1, Sayi 1, Yil 2001-2002
2. Aksin, D. Y. "A high-precision high-resolution WTA-MAX circuit of $O(N)$ complexity", Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on, Vol. 49, Issue 1, pp 48-53, Jan 2002.
3. Aksin, D.Y. Al-Shyoukh, M. Maloberti, F. "Switch Bootstrapping for Precise Sampling Beyond Supply Voltage", Solid-State Circuits, IEEE Journal of, Vol 41, Issue 8, pp 1938-1943, Aug 2006.

Honorary and Awards

1. From Alcatel - Teletaş Corporation, for the success of the project DOV receiving TUBITAK-TTGV-TUSIAD Technology Award in 2000.
2. From Alcatel - Teletaş Corporation, for the success of the project PIPS.

Patents

1. Netaş – DM32. Patent application, 29/05/1998 - 1998 / 00952, EP PCT/TR99/00020, Bursa Patent Ltd. Şti.
2. US Patent 20060202879, “Analog-to-digital converter with input signal range greater than supply voltage and extended dynamic range”, September 14, 2006
3. US Patent 20060202742, “Bootstrapping circuit capable of sampling inputs beyond supply voltage”, September 14, 2006
4. US Patent 20060202736, “Bootstrapped switch for sampling inputs with a signal range greater than supply voltage”, September 14, 2006
5. US Patent 20060202735, “Bootstrapped switch with an input dynamic range greater than supply voltage”, September 14, 2006

Memberships

IEEE, IEEE Solid State Society, IEEE Circuits and Systems Society, TMMOB Chamber of Turkish Electrical Engineers