

ELE 501E: Noise in Electronic Devices and Systems

Instructor: Asst. Prof. Dr. Mustafa Berke Yelten Email: yeltenm@itu.edu Office: EEF 2317

Recommended text: Stephen A. Maas, "Noise in Linear and Nonlinear Circuits", Artech House, Norwood, Massachusetts, USA, 2005

Course description: In this course, you will learn about noise in semiconductor devices and circuits. Noise is an inherent concept in electronic system design. It acts as a performance limiter and should be accounted for to meet the specifications. To better understand noise, first random processes will be reviewed. Then, fundamentals of noise analysis will be provided followed by the noise models of different transistor types. Noise analysis of circuits is explained. This will lead to the design of low noise amplifiers (LNAs) and noise optimization/cancellation techniques employed in LNAs. After discussing the noise response of main RF building blocks, last weeks will be reserved for presentations prepared by students based on their term paper topics.

Grading: 25% Midterm, 40% Final exam, 25% Term paper & presentation, 10% HW

Topics:

09/17:	Fundamentals of Noise Analysis and Random Processes	
09/24:	Festival of Sacrifice – No class	
10/01:	Noise Colors and Physical Sources of Noise	
10/08:	Noise Measurement Techniques	
10/15:	Noise Models of Semiconductor Devices	HW1
10/22:	Noise Theory of Linear and Nonlinear Circuits	HW1 return
10/29:	Republic Day – No class	
11/05:	Midterm	
11/12:	Amplifier Noise Calculations	HW2
11/19:	Low Noise Amplifiers & Noise Optimization Cancellation Techniques	HW2 return
11/26:	Noise of Mixers and Frequency Multipliers	Term Paper assigned
12/03:	Noise of Oscillators and Phase Locked Loops (PLLs)	
12/10:	Student term paper presentations	
12/17:	Student term paper presentations	
12/24:	No class	